



RUTT ELECTROPHYSICS

21-29 West 4th Street, New York, N.Y. 10012 (212) 982-8300

IC-1 MC 1494
IC-2 LM 318
IC-3 LM 318
IC-4 LM 318

D-1 - IN 914
D-2 - IN 914
D-3 - IN 914
D-4 - IN 914
D-5 - IN 914
D-6 - IN 914

Q-1 NPN 2N3568
Q-2 PNP 2N3638
Q-3 NPN 2N3568
Q-4 PNP 2N3638

C-1 10 PF
C-2 10 PF
C-3 15 of 20V
C-4 15 of 20V
C-5 10 PF
C-6 .1 cer
C-7 .1 cer
C-8 10 PF
C-9 .1 cer

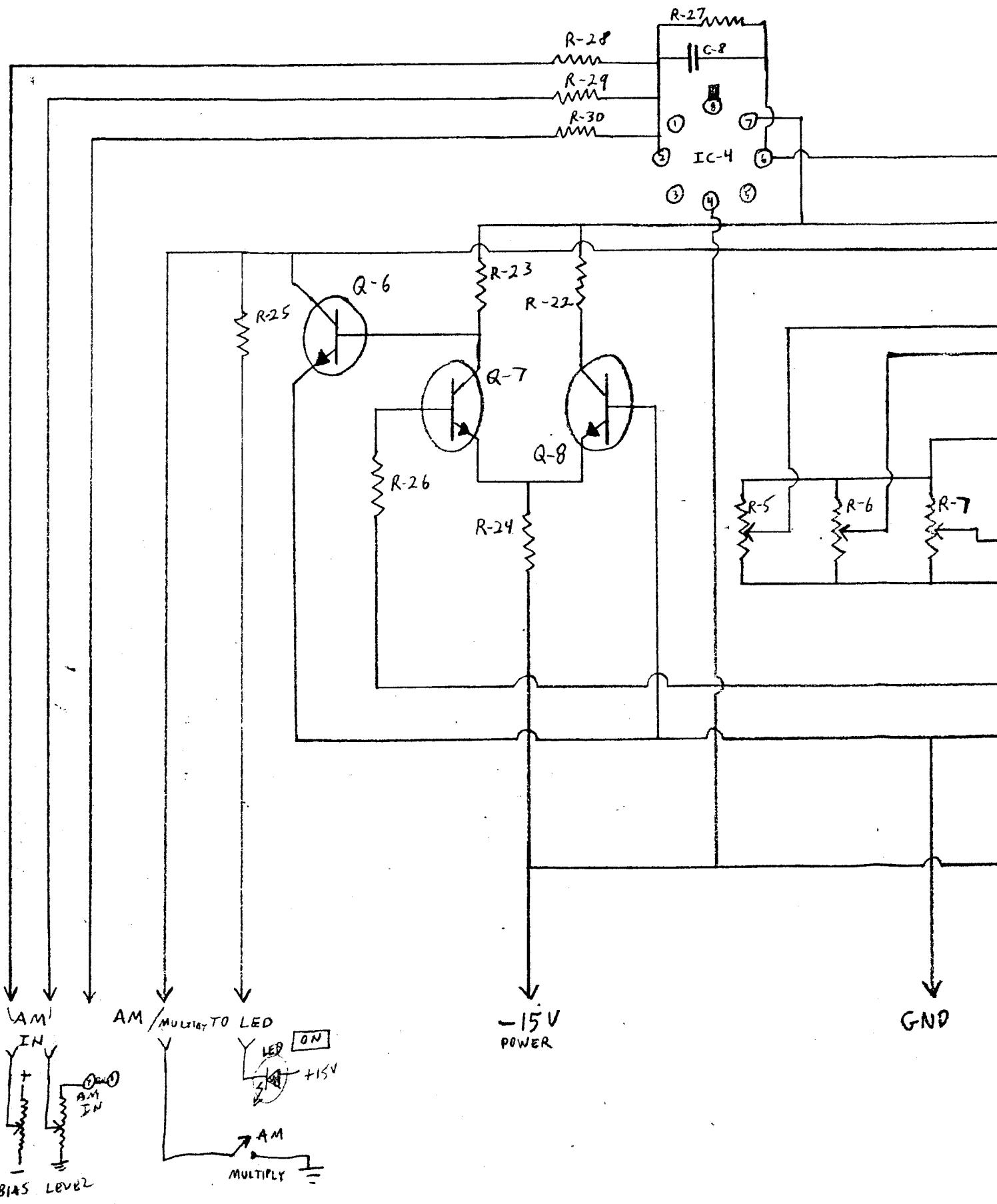
PC 127A

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Q-5 FET 2N5462
Q-6 NPN 2N3568
Q-7 NPN 2N3568
Q-8 NPN 2N3568

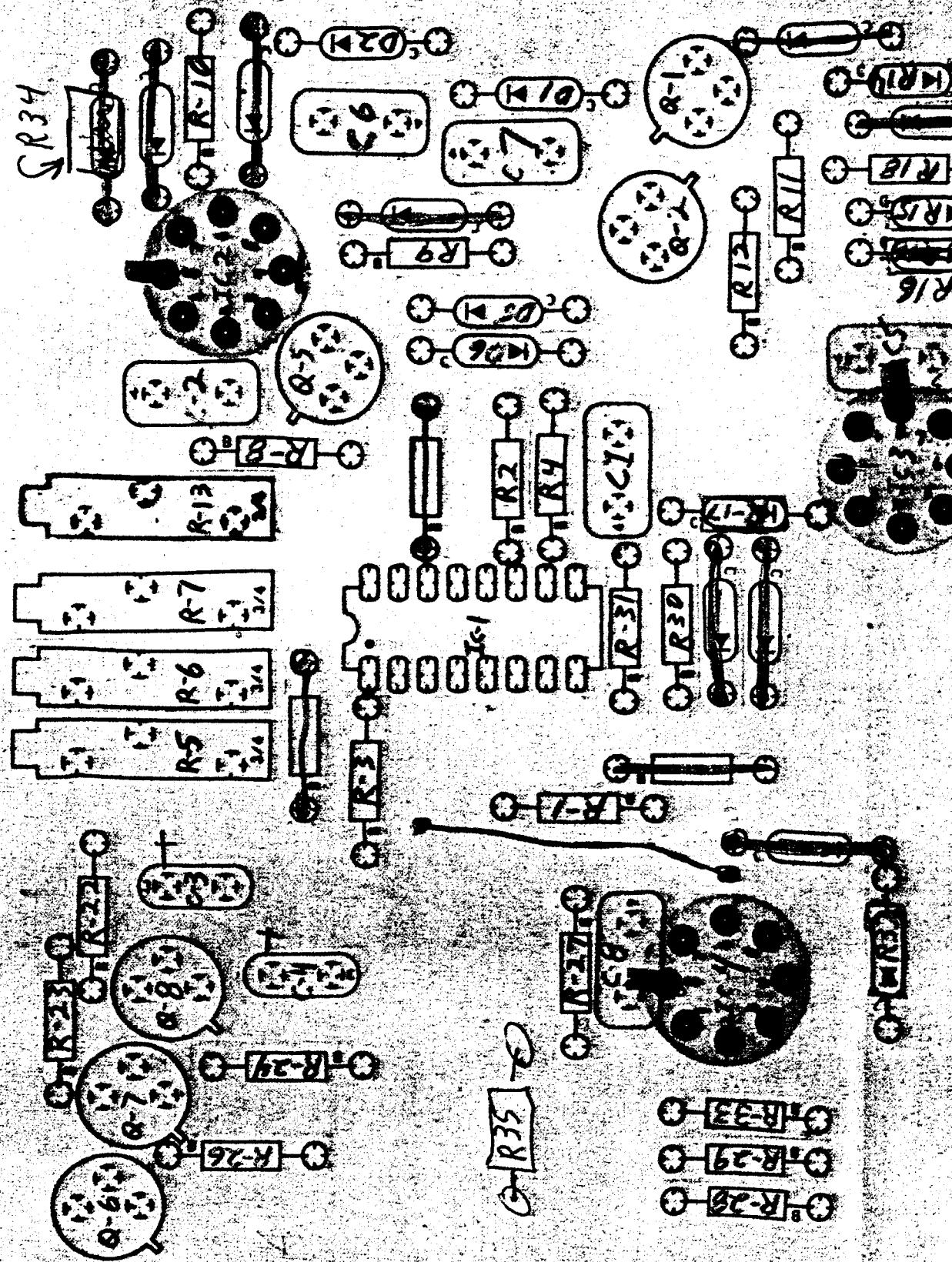
R-1 27 K
R-2 15 K
R-3 12 K
R-4 510 Ω
R-5 20K pot
R-6 20K pot
R-7 20K pot
R-8 47 K
R-9 10 K
R-10 10 K
R-11 10 Ω
R-12 10 Ω

R-13 20K pot
R-14 150 Ω
R-15 10K
R-16 10K
R-17 10K
R-18 10K
R-19 10 Ω
R-20 10 Ω
R-21 150 Ω
R-22 6.8 K
R-23 20 K
R-24 6.8 K
R-25
R-26 10K
R-27 20K
R-28 15K
R-29 10K
R-30 2.2K
R-31 10K
R-32 4.7K
R-33 omit
R-34
R-35 10K
R-36 4.7K



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21) Charge G-1 from 0 to -28 to ± 15

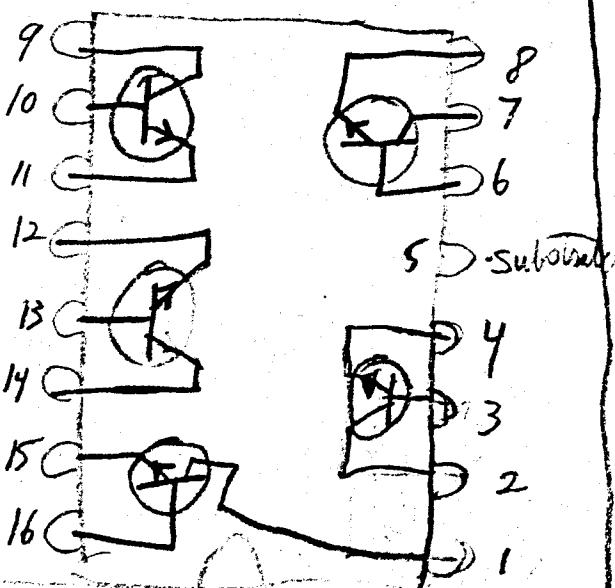
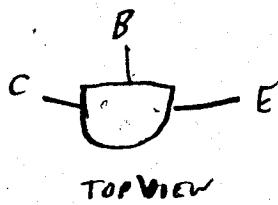
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VIDEO DRIVER

CORRECTION

- 1) R30, IC-10 pin 2, Q-4 Base
ARE NOT SUPPOSED TO GO TO GND
- 2) IC-10 Pin 7 + OTHER COMPONENT
on same wire SHOULD ALSO GO TO
-15V
- 3) R-44 IS NOT GROUNDED

CA-3083

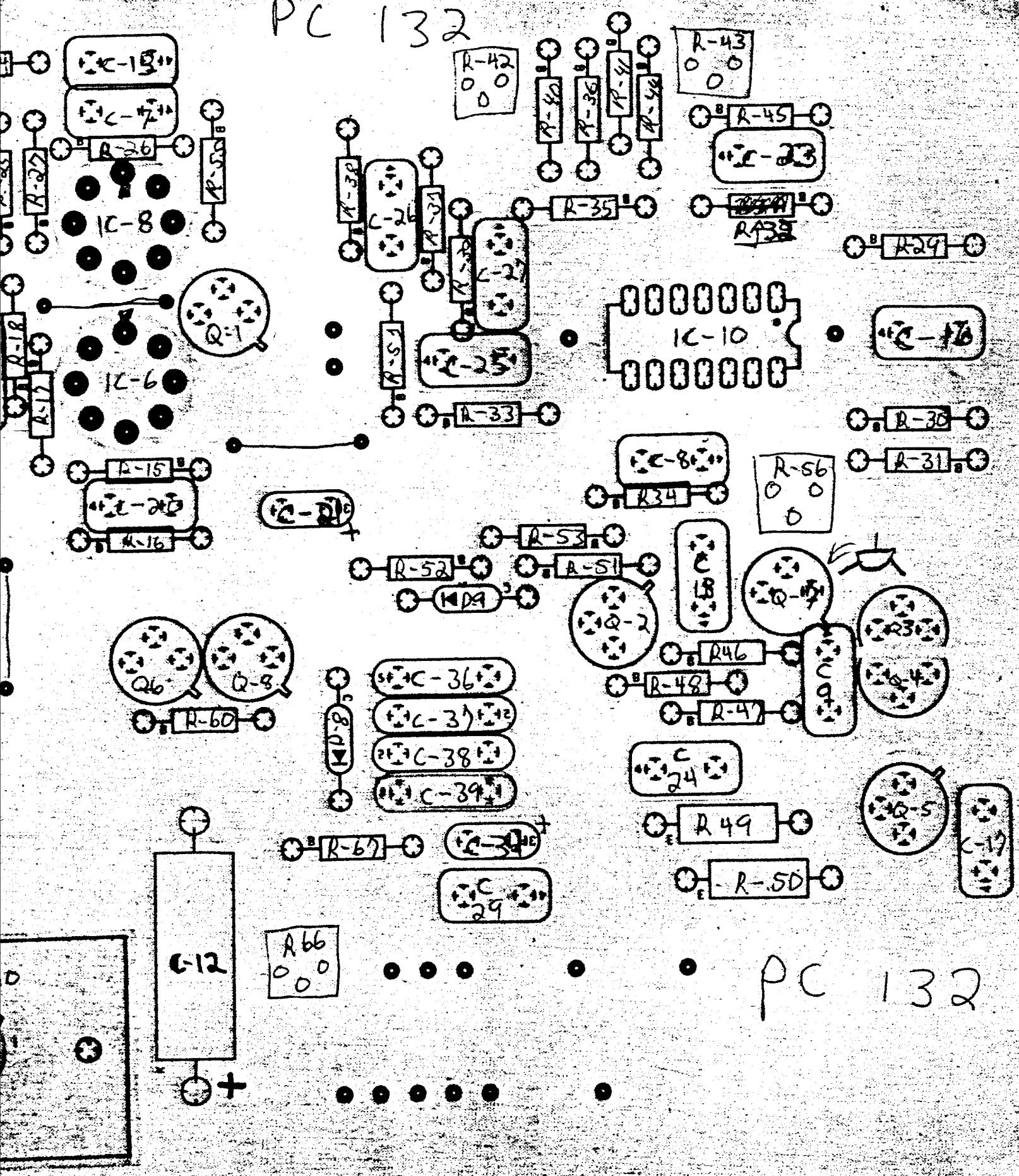


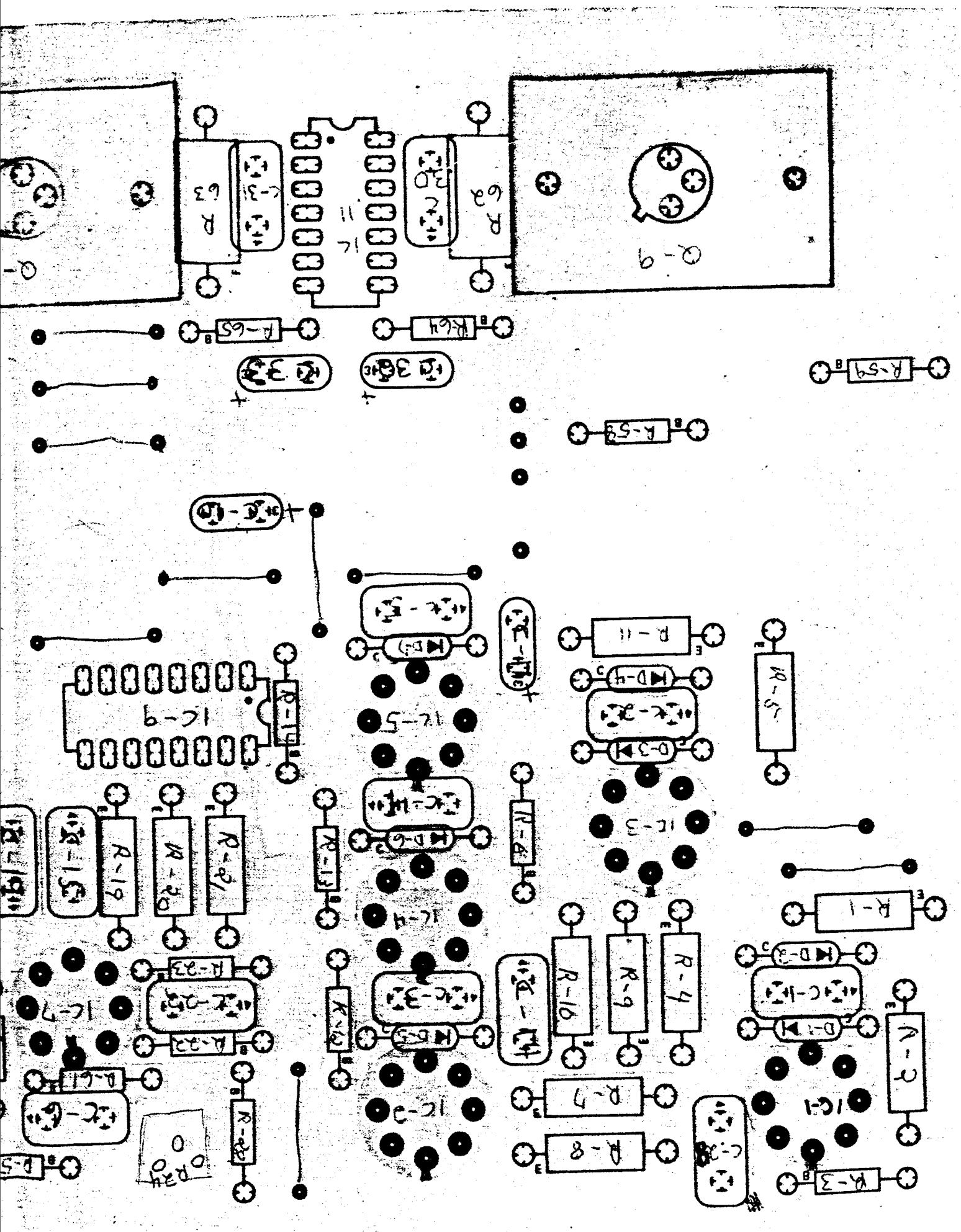
19) Reduce VALUE of R61
20) PUT LIMIT POT on INTENSITY (OCV) & BIAS POT IN SOT EXT INT. AT SPECIFIC R
17) PUT 47K Resistor IN SERIES WITH C11 AND C12
18) 100 10K Res IN SERIES WITH +45V IN
15) ADD 10F CAP TO IC-PIN 2-6
16) ADD 330 RESISTORS BETWEEN GND & GND ON + & - 28V TA 564501

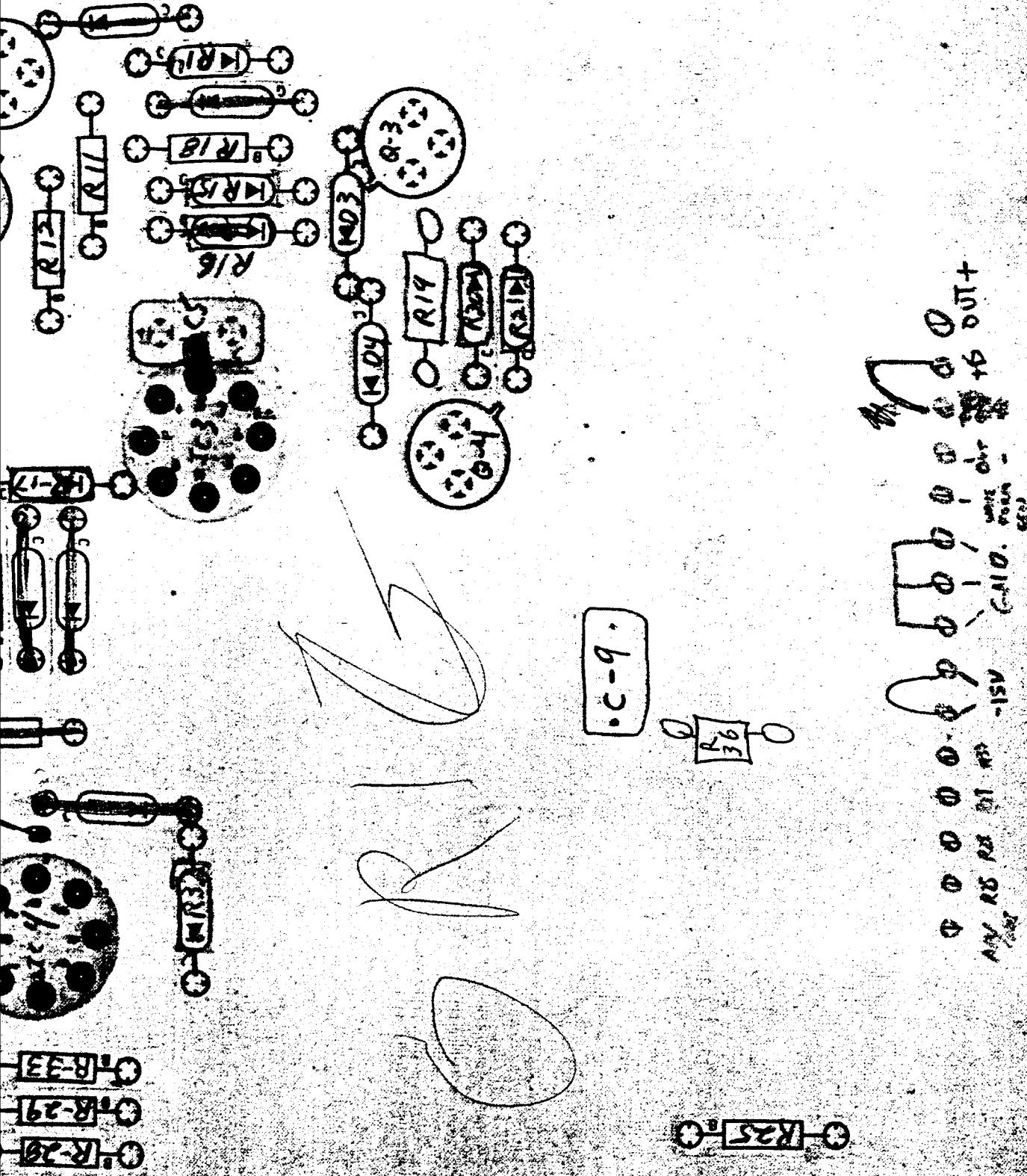
- 4) IC 2 Pin 2 & 4 are MESSED UP
CONNECT PIN 4 TO B-
CUT B- FROM PIN 2 - CONNECT PIN 2 TO 0.05±23

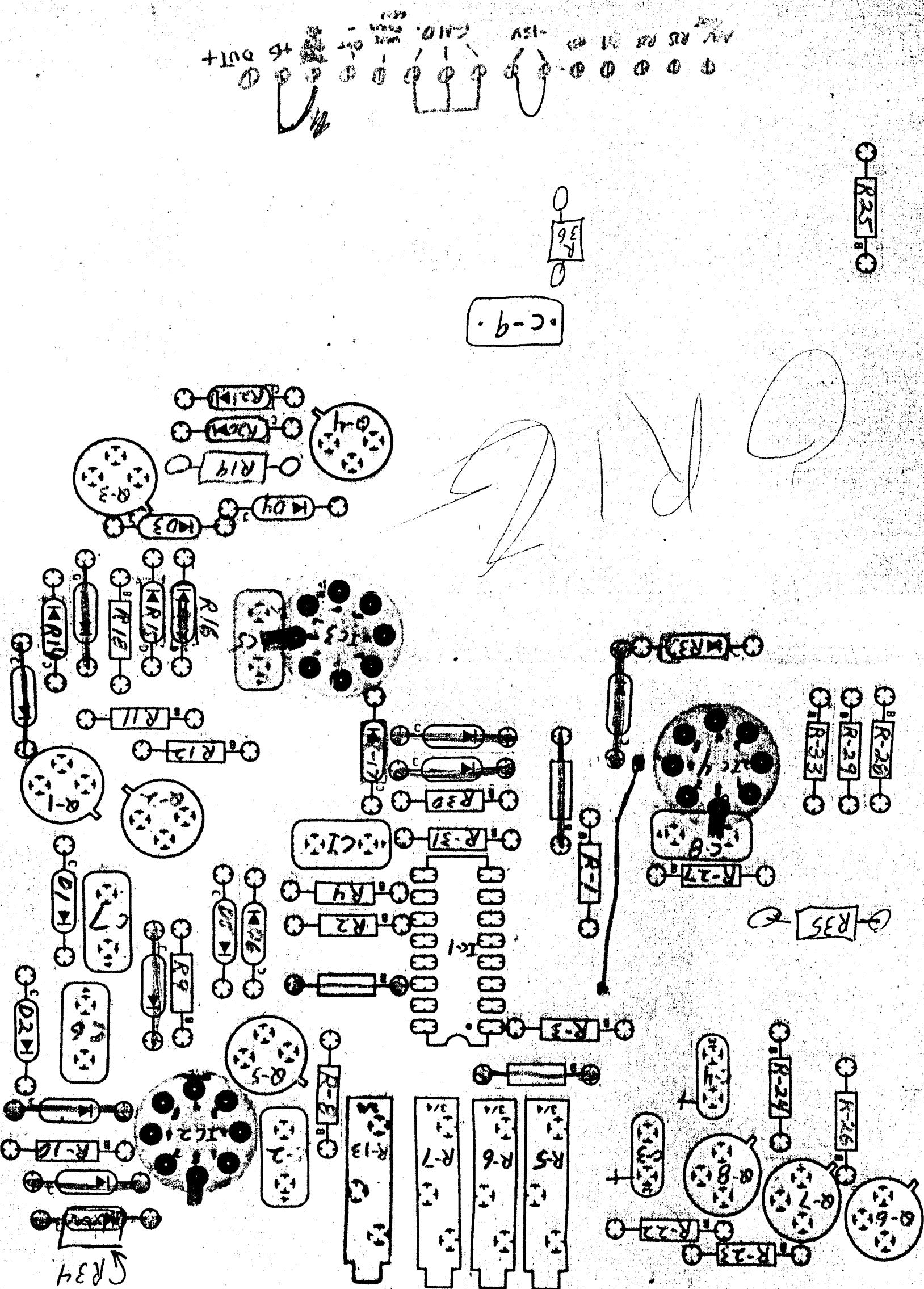
OVER

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